



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Robert F. Wallace et al.
Title: Use of Small Electronic Circuit Cards With Different Interfaces in an Electronic System
Application No.: 09/633,089 Filing Date: August 4, 2000
Examiner: Frech, Karl D. Group Art Unit: 2876
Docket No.: SNDK.149US0 Conf. No.: 1104

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, a representation that a search has been made, (other than as indicated by any cited document); or

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3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).


Three references are being cited. PCT application publication no. WO 00/70553 was previously cited in a Supplemental Information Disclosure Statement filed October 17, 2002 but the form PTO-1449 returned with the next Office Action mailed February 28, 2003 was not initialed by the examiner. Therefore, this reference is being resubmitted, and its counterpart U.S. patent no. 6,496,381 is being cited for the first time.

A document "SD Memory Card Simplified Specifications" dated January 2000 is also being cited by this Disclosure Statement. These Specifications describe the physical structure and electrical connections of the external contacts of the SD memory card promoted and licensed by the SD Group through the SD Card Association. The cited Specifications were prepared for unrestricted public distribution but the extent and timing of any such distribution are not known.

EXPRESS MAIL
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Respectfully submitted,


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November 10, 2003
Date

U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	SNDK.149US0	09/633,089
Use several sheets if necessary)	Applicants	Conf. No.
	Robert F. Wallace et al.	1104
	Filing Date	Art Group
	August 4, 2000	2876

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	6,496,381 B1	Dec. 17, 2002	Groeger			

U.S. Published Patent Application Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	2	WO 00/70553	Nov. 23, 2000	WIPO			Abstract	X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	3	SD Group, "SD Memory Card Simplified Specifications, Part 1, Physical Layer Specification", Version 0.96, January 2000, 28 pages.						

Examiner	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.